



# Siemens TI405 Series

## Overview

Maple Systems' MAP Family & OIT Family Operator Interface Terminals (Maple OITs) communicate with Siemens SIMATIC TI405 Series Programmable Logic Controllers (PLCs) using the K Sequence protocol.

Compatible PLCs	
PLC Family	PLC Model
Siemens SIMATIC TI405 Series	425, 435

## Communications Cable

The Maple OIT should be connected to the communications port located on the programmable controller. Refer to Technical Note 1061 for information on communication cable part numbers and cable assembly instructions. If you will be assembling your own communications cable, cable assembly instructions are also available on our web site at [www.maple-systems.com](http://www.maple-systems.com).

**WARNING:** If your communications cable is not wired exactly as shown in our cable assembly instructions, damage to the Maple OIT or loss of communications can result.

## PLC Settings

The Siemens TI405 PLC has a three position switch on the CPU which can change the operating mode of the PLC (RUN/TERM/STOP). Set this switch to TERM mode when communicating with the OIT.

The Password must match the Password setting in OITware-200 or MAPware-100 (Model 435 CPU only).

# Accessible PLC Memory

## PLC Register Memory

The following table lists the PLC register memory ranges that Maple's OITs are able to access. Please note that your PLC's memory range may be *smaller* or *larger* than that supported by Maple's OITs. The following PLC register memory is displayable in 16-bit or 32-bit formats on the Maple OIT.

PLC Register Address	PLC Register Description
T0 to T177	Timer Accumulators (present values)
CT0 to CT177	Counter Accumulators (present values)
V1400 to V7377	User Data Registers
V7400 to V7777	System Parameter Registers

## PLC Discrete Memory

The following table lists the PLC discrete memory ranges that Maple's OITs are able to access. Please note that your PLC's memory range may be *smaller* or *larger* than that supported by Maple's OITs. The following discrete PLC memory is displayable in single-bit and bank formats on the Maple OIT.

PLC Bit Address	PLC Bit Description
X0 to X477	Inputs
Y0 to Y477	Outputs
GX0 to GX777	Remote Inputs and Outputs
C0 to C737	Control Relays
S0 to S577	Stages
SP0 to 137, 320 to 617	Special/Status Relays

# OITware-200 Settings

The following table lists the communications settings that must be configured in OITware-200. Please note:

- the Default column lists OITware-200's default setting; your PLC's default may be different
- the Options column lists OITware-200's options; your PLC may not support every option

Name	Default	Options	Important Notes
Baud Rate	9600	19200, 9600, 4800, 2400, 1200, 600, 300	Must match the PLC port settings. Use the fastest baud rate supported by both.
Parity	Odd	Even, Odd, None, Mark, Space	Must match the PLC port settings.

Data Bits	8	7, 8	Must match the PLC port settings.
Stop Bits	1	1, 2	Must match the PLC port settings.
Status Coils	C500	C0 to 1777	Must be within the PLC's supported memory range.
Address, Source Address, Destination Address	N/A		
Password	00000000 8 Zeros	Eight-digit password using numbers 0-9	Must exactly match the access password that is set in the PLC (Model 435 CPU only). Otherwise, the Maple OIT will not be able to log on to the PLC.
Message Request Register	V5000	V1400 to 7377, V7400 to 7777	Must be within the PLC's supported memory range.
Current Message Register (optional)	V5002	V1400 to 7377, V7400 to 7777	Must be within the PLC's supported memory range.
Function Key Coils (optional)	C520	C0 to 1777	Must be within the PLC's supported memory range.
Screen Dependent Function Key Coils (optional)	C560	C0 to 1777	Must be within the PLC's supported memory range. Applies to OITs with Screen Dependent Function Keys.
Control Key Coils (optional)	C600	C0 to 1777	Must be within the PLC's supported memory range.
Status LED Coils (optional)	C0	C0 to 1777	Must be within the PLC's supported memory range. Applies to OITs with Status LEDs.
Function Key LED Coils (optional)	C530	C0 to 1777	Must be within the PLC's supported memory range. Applies to OITs with Function Key LEDs.

# MAPware-100 Settings

The following table lists the communications settings that must be configured in MAPware-100. Please note:

- the Default column lists MAPware-100's default setting; your PLC's default may be different
- the Options column lists MAPware-100's options; your PLC may not support every option

Name	Default	Options	Important Notes
Baud Rate	9600	19200, 9600, 4800, 2400, 1200, 600, 300	Must match the PLC port settings. Use the fastest baud rate supported by both.
Parity	Odd	Even, Odd, None, Mark, Space	Must match the PLC port settings.
Data Bits	8	7, 8	Must match the PLC port settings.
Stop Bits	1	1, 2	Must match the PLC port settings.
Status Coils	C500	C0 to 1777	Must be within the PLC's supported memory range.
Address, Source Address, Destination Address	N/A		
Password	00000000 8 Zeros	Eight-digit password using numbers 0-9	Must exactly match the access password that is set in the PLC (Model 435 CPU only). Otherwise, the Maple OIT will not be able to log on to the PLC.
Message Request Register	V5000	V1400 to 7377, V7400 to 7777	Must be within the PLC's supported memory range.
Function Key Coils (optional)	C520	C0 to 737	Must be within the PLC's supported memory range.

## Important PLC Memory Considerations

If your PLC's memory range is smaller than the range supported by Maple's OITs, it is possible to configure the Maple OIT to monitor a PLC memory address which does not exist. Since this can cause unpredictable results, when you configure the Maple OIT please ensure that all selected PLC memory addresses are valid for your PLC model.

Do not configure the Maple OIT to write to any PLC memory address which should only be written to by the PLC.

**On using Bank 8 or Bank 16 formats**

When using these formats, each PLC coil (bit) is individually displayed in terms of 1 and 0, with the lowest addressed coil displayed in the right-most position in the field. Therefore, if using coils X0-17, then X0 is the least significant bit displayed in the right-most position and X17 is the most significant bit displayed in the left-most position. When selecting the address of the register, the address should be on a byte boundary. Since all addresses are expressed in octal, the first coil's address must end in 0 to make a valid bank address. Y0-Y17, C160-177, S460-477 are all examples of valid banks.

The OIT uses the password to gain read and write access in the PLC's internal data memory. If no password is entered, the OIT creates a null password of "000000000." If this password does not exactly match the access password in the PLC, the OIT will be unable to log on to the PLC.

**NOTE:** Since this is a password field, a full eight-digit number must be entered, including any leading zeroes.